

Examiner Signature		Date Considered	10/5/04
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Substitute for form 1449B/PTO		<b>Complete If Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	Not Yet Known 10 689 054
		Filing Date	01/11/03
		First Named Inventor	SHOR, Joseph S. et al.
		Group Art Unit	Not Yet Known 2816
		Examiner Name	Not Yet Assigned TPA
Sheet 2 of 2	Attorney Docket Number	P-6062-US	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
J		P. E. Allen and D. R. Holberg in "CMOS Analog Circuit Design"  (Oxford University Press, 2002), pp. 259	<input type="checkbox"/>
K		J. S. Shor, Y. Sofer, Y. Polansky, and E. Maayan, in /SCAS 2002, Phoenix, Arizona	<input type="checkbox"/>
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